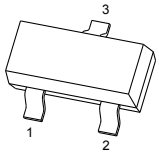
Leiditech

2N7002K

N-Channel Enhancement Mode Power MOSFET

$V_{(BR)DSS}$	$R_{DS(on)}$MAX	I_D
60 V	3Ω@10V	300mA
	4 Ω@4.5V	

SOT-23



1. GATE
2. SOURCE
3. DRAIN

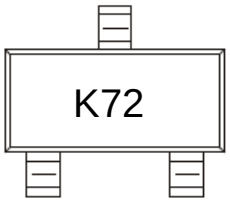
FEATURE

- High density cell design for Low $R_{DS(on)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability
- ESD protected

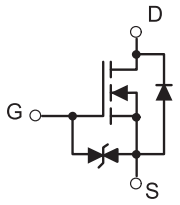
APPLICATION

- Load Switch for Portable Devices
- DC/DC Converter

MARKING



Equivalent circuit



MOSFET MAXIMUM RATINGS (T_a = 25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source voltage	60	V
V_{GS}	Gate-Source voltage	±20	V
I_D	Drain Current	300	mA
P_D	Power Dissipation	0.35	W
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature	-55~+150	℃
$R_{\theta JA}$	Thermal Resistance fromJunction to Ambient	357	℃ /W

MOSFET ELECTRICAL CHARACTERISTICS

T_a=25℃ unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Static Characteristics						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0V, I_D =250μA	60			V
Gate Threshold Voltage*	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =1mA	1	1.3	2.5	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} =48V,V_{GS} = 0V			1	μA
Gate –Source leakage current	I_{GSS1}	V_{GS} =±20V, V_{DS} = 0V			±10	μA
	I_{GSS2}	V_{GS} =±10V, V_{DS} = 0V			±200	nA
	I_{GSS3}	V_{GS} =±5V, V_{DS} = 0V			±100	nA
Drain-Source On-Resistance*	$R_{DS(on)}$	V_{GS} = 4.5V, I_D =200mA		3	4	Ω
		V_{GS} =10V,I_D =500mA		2	3	Ω
Diode Forward Voltage	V_{SD}	V_{GS}=0V, I_S=100mA			1.2	V
Recovered charge	Q_r	V_{GS}=0V,I_S=300mA,V_R=25V, dI_S/dt=-100A/μS		30		nC
Dynamic Characteristics**						
Input Capacitance	C_{iss}	V_{DS} =10V,V_{GS} =0V,f =1MHz			40	pF
Output Capacitance	C_{oss}				30	pF
Reverse Transfer Capacitance	C_{rss}				10	pF
Switching Characteristics**						
Turn-On Delay Time	$t_{d(on)}$	V_{GS}=10V,V_{DD}=50V,R_G=50Ω, R_{GS}=50Ω, R_L=250Ω			10	ns
Turn-Off Delay Time	$t_{d(off)}$				15	ns
Reverse recovery Time	t_{rr}	V_{GS}=0V,I_S=300mA,V_R=25V, dI_S/dt=-100A/μS		30		ns
GATE-SOURCE ZENER DIODE						
Gate-Source Breakdown Voltage	BV_{GSO}	I_{gs}=±1mA (Open Drain)	±21.5		±30	V

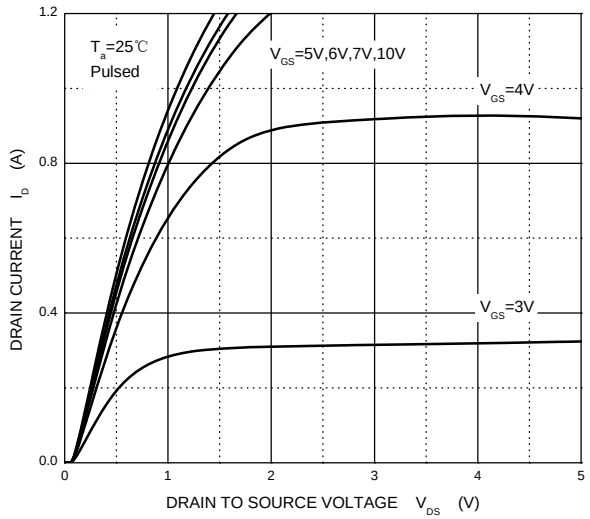
Notes :

*Pulse Test : Pulse Width ≤300μs, Duty Cycle ≤2%.

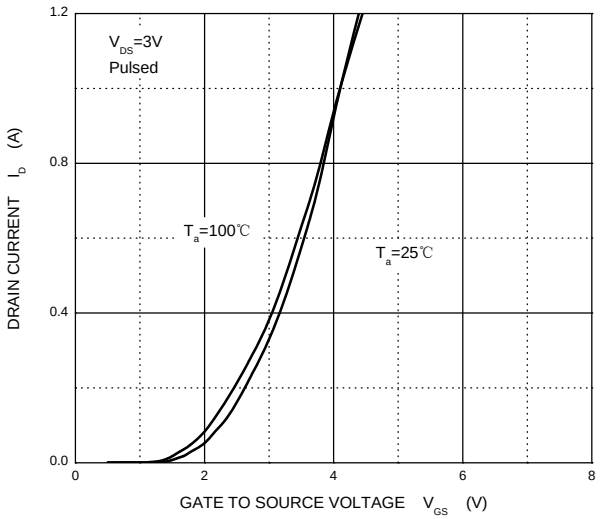
**These parameters have no way to verify.

Typical Characteristics

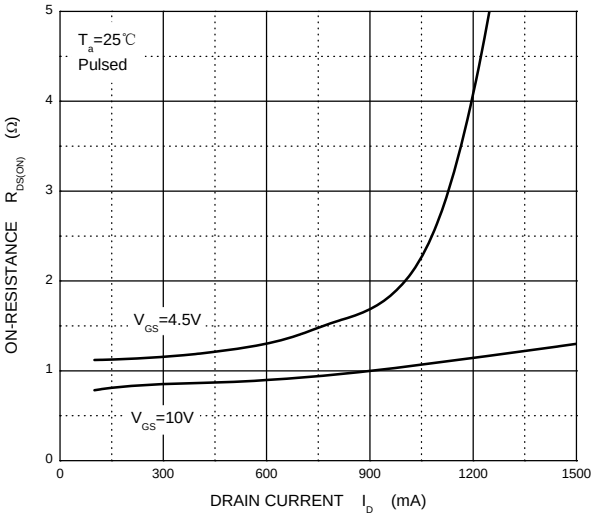
Output Characteristics



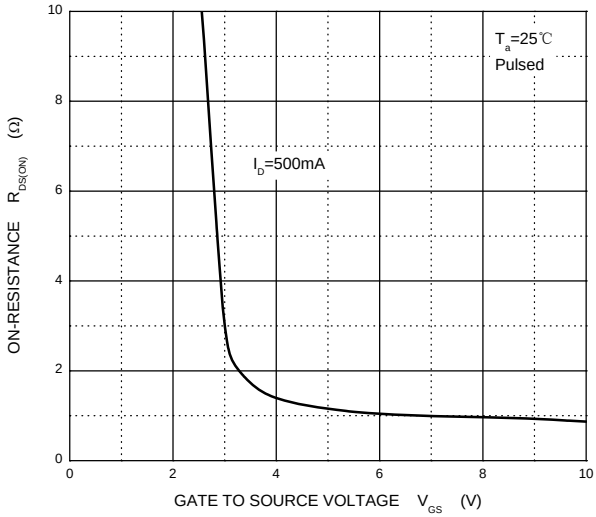
Transfer Characteristics



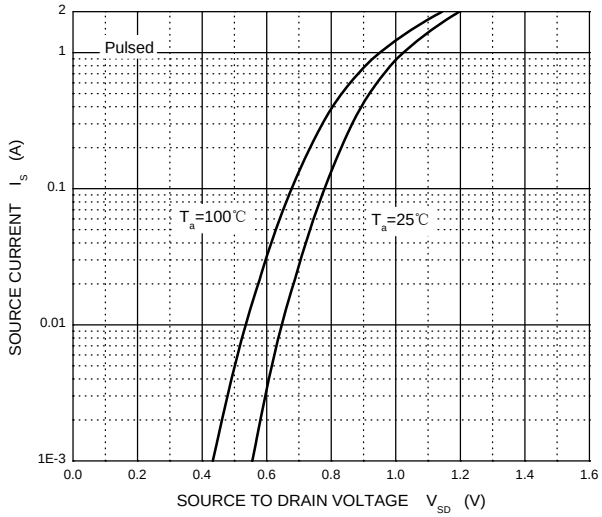
$R_{DS(ON)}$ —— I_D



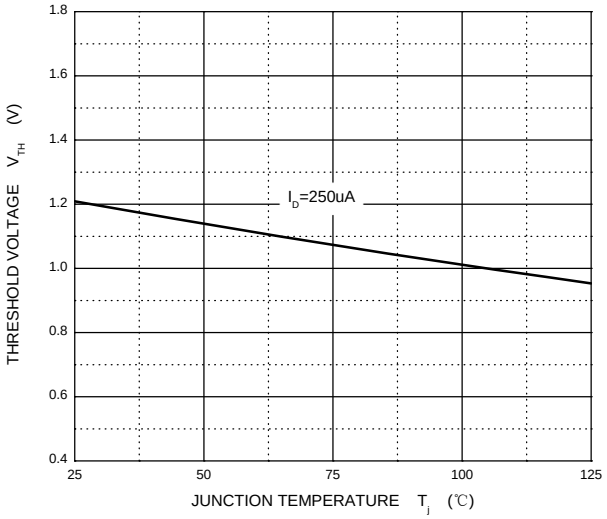
$R_{DS(ON)}$ —— V_{GS}



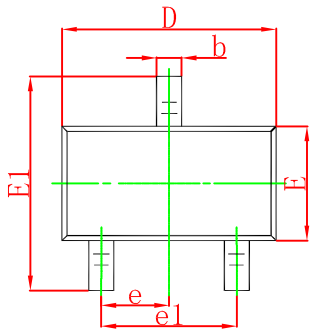
I_S —— V_{SD}

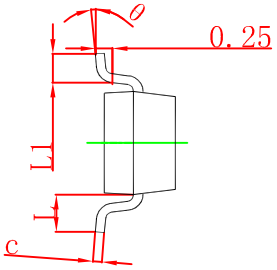


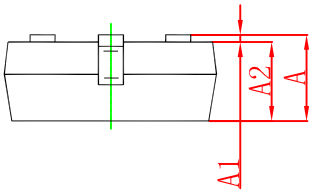
Threshold Voltage



SOT-23 Package Outline Dimensions

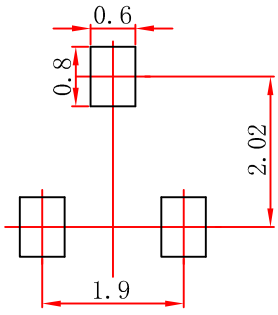






Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

SOT-23 Suggested Pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.

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